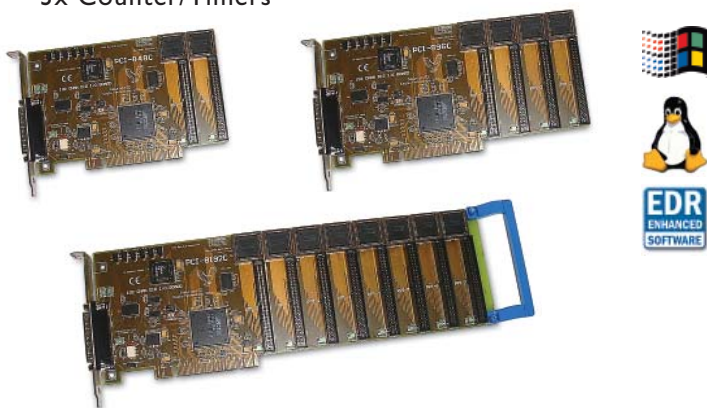


3x Counter/Timers



DESCRIPTION

The **PCI-848**, **PCI-896** and **PCI-8192** are PCI-bus digital I/O boards based on the industry standard 82C55 PPI chip. Each board is available in two versions, an -A and -C version. The -C version has 3x 16-bit counter-timers, an optically isolated line and 10x interrupt sources.

FEATURES

- 48/96/192 Channel digital inputs/outputs (6/12/24-ports)
- 3 Channel 16-bit counter/timers (C-version)
- 1 Channel opto-isolated external trigger input (C-version)
- 10 interrupt sources (C-version)
- 82C55 compatible digital input/output interface
- Windows 98/ME/2000/XP/NT OS Support
- Linux OS Support
- WaveView for Windows Data Acquisition & Logging Software
- Labview, Testpoint and VEE Pro Drivers
- PCI-bus revision 2.2 compliant

IDC50

PC7	1	2	DGND
PC6	3	4	DGND
PC5	5	6	DGND
PC4	7	8	DGND
PC3	9	10	DGND
PC2	11	12	DGND
PC1	13	14	DGND
PC0	15	16	DGND
PB7	17	18	DGND
PB6	19	20	DGND
PB5	21	22	DGND
PB4	23	24	DGND
PB3	25	26	DGND
PB2	27	28	DGND
PB1	29	30	DGND
PB0	31	32	DGND
PA7	33	34	DGND
PA6	35	36	DGND
PA5	37	38	DGND
PA4	39	40	DGND
PA3	41	42	DGND
PA2	43	44	DGND
PA1	45	46	DGND
PA0	47	48	DGND
+5V_FUSED	49	50	DGND

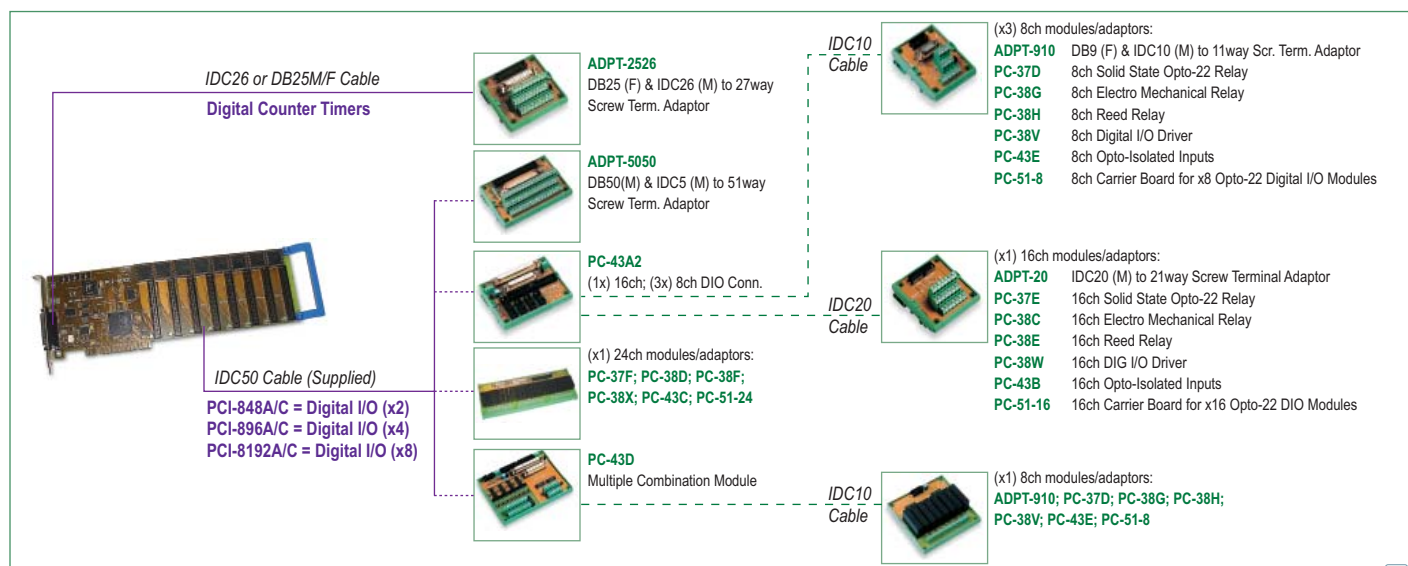
DB 25M (C-Models only)

NC	14	1	+12V_FUSED
NC	15	2	-12V_FUSED
NC	16	3	NC
NC	17	4	CLK0
OUT0	18	5	GATE0
CLK2	19	6	OUT2
GATE2	20	7	CLK1
GATE1	21	8	OUT1
NC	22	9	DGND
NC	23	10	+5V_FUSED
EXT_TRIG_RET	24	11	NC
FREQ_IN	25	12	EXT_TRIG
	26	13	FREQ_OUT



Supplied with
IDC50 Cable

Optional Accessories Diagram



Specifications

DIGITAL I/O - PPI	
Number of I/O Lines:	PCI-848: 48, 6 ports x 8-bit (82C55/TTL) PCI-896: 96, 12 ports x 8-bit (82C55/TTL) PCI-8192: 192, 24 ports x 8-bit (82C55/TTL)
Logic Levels:	Input Low Voltage: -0.5V to 0.8V Input High Voltage: 2.0V to 5.0V Output High Voltage Min: 2.4V Output Low Voltage Max: 0.45V Maximum Output Current: 2mA
COUNTER-TIMER (C-Version)	
Number of Counters:	3 (16-bit resolution) (82C54/TTL)
Clock Source:	Jumper Selectable Scaled Internal up to 8MHz (External)
Gate Source:	Jumper Selectable Software Controlled (External)
OPTO-ISOLATED INPUT CHARACTERISTICS (C-Versions)	
Number of Lines:	1 (Analog/TTL)
Logic Levels:	O/P High Voltage -Logic1: 3.1V to 28V O/P Low Voltage -Logic0: 0V to 3V On Current: 20mA Max Forward Current: 50mA
EXTERNAL INTERFACE	
Connector - External:	DB25 Male (C-Version)
Connector - Internal:	PCI-848: 2x IDC50 Male PCI-896: 4x IDC50 Male PCI-8192: 8x IDC50 Male
DIMENSIONS	
PCI-848: 162 mm x 97 mm PCI-896: 208 mm x 97 mm PCI-8192: 302 mm x 97 mm	
POWER REQUIREMENTS	
+5V Internal PCI-848: 350mA typical PCI-896: 400mA typical PCI-8192: 500mA typical	

Optional Accessories

OPTION 1:

ADPT-2526	DB25 (F) & IDC26 (M) to 27way Screw Terminal Adaptor
DB25M/F	DB25 (M) to DB25 (F) Screened Cable
IDC26	26way Ribbon Cable

OPTION 2:

ADPT-5050	DB50 (M) & IDC50 (M) to 51way Screw Terminal Adaptor
IDC50	50way Ribbon Cable (Supplied)

Ordering Information

Supplied with EDR Enhanced Software and (2x) IDC50 Internal Cable

PCI-848A	48 Channel Digital I/O Board
PCI-848C	48 Channel Digital I/O Board with 3x Counter/Timers

Supplied with EDR Enhanced Software and (4x) IDC50 Internal Cable

PCI-896A	96 Channel Digital I/O Board
PCI-896C	96 Channel Digital I/O Board with 3x Counter/Timers

Supplied with EDR Enhanced Software and (8x) IDC50 Internal Cable

PCI-8192A	192 Channel Digital I/O Board
PCI-8192C	192 Channel Digital I/O Board with 3x Counter/Timers