PC104-30F/G

12-bit A/D with Digital I/O & Output

Embedded PCI Data Acquisition





DESCRIPTION

The PC104-30F/G is a high performance multi-function I/O board for embedded systems.

All the low noise A/D, D/A and DIO functions needed for any control or monitoring application are fitted. It includes 16 analog input channels with 12-bit resolution and 100kHz or 330kHz sampling rate. Software programmable gains of 1,10,100,1000 or 1,2,4,8 are selectable for measuring low-level signals. As an option, the PC104-30F/G boards can be ordered with four 12-bit D/A outputs with current sensing outputs.

All boards feature a channel list which allows the automatic scanning of analog input channels. Jumperless operation means interrupts and DMA are configured via software. Flexible digital I/O capabilities consisting of 24 lines in three ports can be configured as inputs or outputs. Also included, is one 16-bit user counter/ timer used to generate or measure frequency and count events or speed.

Although designed to be full PC/104 rev 2.3 compliant this board will work with any PC-based PC/104-expandable embedded controllers. It is compatible with the ISA based PC-30F/G boards allowing easy migration from ISA to the PC/104 Based PC104-30F/G.

With the PC104-30F/G we provide support for both our 16-bit (EDR) and 32bit (EDRE) Software Developer's Kit. Under EDRE, the board can be used with our WaveView for Windows DAQ software package.

For ease of use, ribbon cables terminated with standard 2.54mm IDC connectors are provided with every board.

FEATURES

- 8 Differential or 16 Single-Ended A/D inputs
- 330kHz or 100kHz sampling rate
- Dual Channel DMA
- Software programmable input ranges: ±5V, ±10V, 0 to 10V
- 12-bit Resolution
- Outband noise filtering
- Optional four 12-bit D/A Outputs
- Software programmable output ranges: $\pm 5V$, $\pm 10V$, 0 to 10, 0 to 13V
- 24 Digital I/O lines (3 ports)
- I User Counter-timer
- Small Dimensions 90x96 mm
- 5VDC only operation
- Windows98/ME/2000/XP/NT OS Support •
- Linux OS Support



Supplied with PC/104 Mounting Kit, IDC26-1 and IDC40-1 Ribbon Cables

Specifications			
ANALOG INPUT	rs (A/D)		
Input Characteris	stics		
Input channels:		16 Single-Ended or 8 Differential	
Overvoltage Pro	tection:	\pm 35V (powered on); \pm 25V (powered off)	
Resolution:		12-Dit (1 In 4096) + 5\/ + 10\/ 0 to 10\/ (G Models)	
input italiges.		\pm 5V, \pm 10V (F models)	
Input Coupling:		DC	
A/D TRANSFER	CHARACTERISTICS		
System Accuracy	/:	± 1 LSB depending on environment	
A/D Linearity	Differential:	+ 3/ LSP mov	
	Integral:	+ 0.05% FS	
	SNR:	84dB typ	
	Full BW:	1MHz	
	Total Harm Dist:	-98dB	
System Accuracy	/:	± 2 LSB depending on environment	
A/D FIFO buffer		16 samples	
Acquisition Mode	es:	Polled I/O, Interrupts, DMA (single/dual)	
Amplifier Chara	cteristics		
Input Impedance	i.	10G Ohm/20pF (On chan)	
Offeeturel		10G Ohm/100pF (Off chan)	
Unset Voltage:		Aujustable to zero	
input Galils	Ranges:	1; 10; 100; 1000 (or 1: 2: 4: 8)	
	Error:	Adjustable to 0	
	Non-linearity:	0.002% (typ), 0.015% (max) [G<1000]	
		0.02% (typ), 0.06% (max) [G=1000]	
	Gain Accuracy:	0.25% max, 0.05% typ	
	CMRR: Monotonicity:	0 to 70°C	
Temperature drif	t:	6ppm/°C (Full Scale)	
		1ppm/°C (Bipolar zero)	
		± 30ppm/°C (Gain typ)	
DYNAMIC CHAI	RACTERISTICS		
Bandwidth (smal	l signal):	1.0MHz (G<1000)	
Full Power Band	width:	1MHz for G<1000 100kHz for G=1000	
Crosstalk:		-85dB, DC to 100kHz	
Sustam Maisa:		1 1 0D (0-1)	
System Noise.		± 1L3D (G-1)	
System Noise.		± 2LSB (G=10)	
	1179	± 1LSB (G=1) ± 2LSB (G=10) ± 4LSB (G=100)	
ANALOG OUTP	UTS	± 2LSB (G=10) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit	
ANALOG OUTP No of Channels: Accuracy:	UTS	± 1LSB (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB	
ANALOG OUTP No of Channels: Accuracy: DNL:	UTS	± 2LSB (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges:	UTS	± 2LSB (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate:	UTS	± 1LSB (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Uninclery ½ LSB twn 11 SB (max)	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error:	UTS	1 LSB (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Biolar: ½ LSB typ, 2 LSB (max)	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges:	UTS	# 1L5B (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ¼ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time:	UTS	# 1L5b (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ¼ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out	UTS 	1 LSB (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O	UTS put:	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Lonic Leve	UTS put: es: els:	11256 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) Hidp: 2.0V (min). 5.0V max	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev	UTS	11256 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max)	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output:	UTS put: es: els:	11256 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink)	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support	UTS put: es: els:	11256 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa	UTS UTS put: es: els: ce v	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types	UTS UTS put: es: els: ce s: ce	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers	UTS put: es: els: ce S: Ce	1126 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Resolution:	UTS UTS put: es: els: ce s: ce	1126 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Resolution: Clock Frequency	UTS put: es: els: : ce : : : : : : : : : : : : :	1126 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D)	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Resolution: Clock Frequency: No of Frequency:	UTS put: es: els:	1126 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 0 cons dia A PB enserging)	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Resolution: Clock Frequency: No of counters:	UTS	1126 (G-1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 lonut CI K 1 Gate & 1 Output	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers Resolution: Clock Frequency: No of counters: User Pins: Compatibility:	UTS	# 1L5b (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Resolution: Clock Frequency: A/D Frequency: No of counters: User Pins: Compatibility: PC104 Interface	UTS	1126 (G-1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers Resolution: Clock Frequency: A/D Frequency: No of counters: User Pins: Compatibility: PC104 Interface Base Address:	UTS	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10Jus max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 20mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL 0 to 1FFFh	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers Resolution: Clock Frequency: A/D Frequency: No of counters: User Pins: Compatibility: PC104 Interface Base Address: No of registers:	UTS	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10Jus max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL 0 to 1FFFh 16 32-bit 4 started and	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers Resolution: Clock Frequency: A/D Frequency: No of counters: User Pins: Compatibility: PC104 Interface Base Address: No of registers: Interrupts: DMA:	UTS	1126 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10Jus max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL 0 to 1FFFh 16 32-bit Auto selected Juto selected	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers Resolution: Clock Frequency: A/D Frequency: No of counters: User Pins: Compatibility: PC104 Interface Base Address: No of registers: Interrupts: DMA: Environmental /	UTS UTS UUTS UUTS UUTS UUTS UUTS UUTS U	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL 0 to 1FFFh 16 32-bit Auto selected Auto selected	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Resolution: Clock Frequency: A/D Frequency: No of counters: User Pins: Compatibility: PC104 Interface Base Address: No of registers: Interrupts: DMA: Environmental / Rel. Humidity:	UTS	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC40 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL 0 to 1FFFh 16 32-bit Auto selected Auto selected 0% to 90% (non-condensing)	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers Resolution: Clock Frequency: A/D Frequency: A/D Frequency: No of counters: User Pins: Compatibility: PC104 Interface Base Address: Interrupts: DMA: Environmental / Rel. Humidity: Operating Temp:	UTS	1126 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC26 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL 0 to 1FFFh 16 32-bit Auto selected Auto selected 0 wo 0% (non-condensing) 0°C to 70°C	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers Resolution: Clock Frequency: A/D Frequency: A/D Frequency: No of counters: User Pins: Compatibility: PC104 Interface Base Address: Interrupts: DMA: Environmental / Rel. Humidity: Operating Temp: Board Dimension	UTS	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC26 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL 0 to 1FFFh 16 32-bit Auto selected Auto selected Wato selected 0 wo 0% (non-condensing) 0°C to 70°C 90.2 mm x 95.9 mm	
ANALOG OUTP No of Channels: Accuracy: DNL: Output Ranges: Thruput Rate: Offset Error: Gain Ranges: Settling Time: Max Current Out DIGITAL I/O No of TTL I/O lin Digital Logic Lev Current Output: Interrupt support External Interfa Connector Types Counter Timers Resolution: Clock Frequency: A/D Frequency: Mo of counters: User Pins: Compatibility: PC104 Interface Base Address: Interrupts: DMA: Environmental // Rel. Humidity: Operating Temp: Board Dimension Weight:	UTS UTS UUTS UUTS UUTS UUTS UUTS UUTS U	1125 (G=1) ± 2LSB (G=10) ± 4LSB (G=100) 4x 12-bit ± 1 LSB ½ LSB max ± 5V, ± 10V, 0 to 10V, 0 to 13V 100kHz Unipolar: ½ LSB typ, 1 LSB (max) Bipolar: ½ LSB typ, 2 LSB (max) x1, x2 10µs max in load 500pF, 2k Ohm 5mA 24 in 3 ports (8255 PPI) High: 2.0V (min), 5.0V max Low: 0.0V (min), 0.8V (max) ± 3mA (source/sink) Mode 0, Mode 1, Mode 2 2mm IDC26 Header for A/D & D/A 2mm IDC26 Header for digital I/O 16-bit 2 or 8 MHz (for A/D) DC to 330kHz 3 (2 used for A/D conversion) 1 Input CLK, 1 Gate & 1 Output TTL 0 to 1FFFh 16 32-bit Auto selected Auto selected Wato selected 0% to 90% (non-condensing) 0°C to 70°C 90.2 mm x 95.9 mm 84 g	

PC104-30F/G

Ordering Information

Supplied with EDR & EDRE Software, PC/104 Mounting Kit and Ribbon Cable(IDC26-1 and IDC40-1)All boards have A/D Inputs, 24 DIO lines and a 16-bit Counter TimerPC104-30G100kHz 16 Channel A/DPC104-30GA100kHz 16 Channel A/D and (4x) 12-bit DACsPC104-30F330kHz 16 Channel A/DPC104-30FA330kHz 16 Channel A/D and (4x) 12-bit DACs				
(IDC26-1 and IDC40-1) All boards have A/D Inputs, 24 DIO lines and a 16-bit Counter Timer PC104-30G 100kHz 16 Channel A/D PC104-30GA 100kHz 16 Channel A/D and (4x) 12-bit DACs PC104-30F 330kHz 16 Channel A/D PC104-30FA 330kHz 16 Channel A/D	Supplied with EDR & EDRE Software, PC/104 Mounting Kit and Ribbon Cable			
All boards have A/D Inputs, 24 DIO lines and a 16-bit Counter TimerPC104-30G100kHz 16 Channel A/DPC104-30GA100kHz 16 Channel A/D and (4x) 12-bit DACsPC104-30F330kHz 16 Channel A/DPC104-30FA330kHz 16 Channel A/D and (4x) 12-bit DACs	(IDC26-1 and IDC40-1)			
PC104-30G 100kHz 16 Channel A/D PC104-30GA 100kHz 16 Channel A/D and (4x) 12-bit DACs PC104-30F 330kHz 16 Channel A/D PC104-30FA 330kHz 16 Channel A/D	All boards have A/D Inputs, 24 DIO lines and a 16-bit Counter Timer			
PC104-30GA 100kHz 16 Channel A/D and (4x) 12-bit DACs PC104-30F 330kHz 16 Channel A/D PC104-30FA 330kHz 16 Channel A/D and (4x) 12-bit DACs	PC104-30G	100kHz 16 Channel A/D		
PC104-30F 330kHz 16 Channel A/D PC104-30FA 330kHz 16 Channel A/D and (4x) 12-bit DACs	PC104-30GA	100kHz 16 Channel A/D and (4x) 12-bit DACs		
PC104-30FA 330kHz 16 Channel A/D and (4x) 12-bit DACs	PC104-30F	330kHz 16 Channel A/D		
	PC104-30FA	330kHz 16 Channel A/D and (4x) 12-bit DACs		

Optional Accessories Diagram



Optional Accessories

OPTION 1:	
ADPT-2526	DB25 (F) & IDC26 (M) to 27way Screw Terminal Adaptor
IDC26-1 (Analog)	26way Ribbon Cable (Supplied)
OPTION 2:	
ADPT-3740	DB37 (M) & IDC40 (M) to 41way Screw Terminal Adaptor
IDC40-1 (Digital I/O)	40way Ribbon Cable (Supplied)





(2.0mm Pitch)