ISA PC-14B

48 Channel DIO with Counter Timer

EDR Software | User Manual | Internal Ribbon Cable

The PC-14B is a general purpose digital input/output board. It is built around two industry standard 8255 PPI devices and a three channel 8254 Counter Timer chip.

Applications for the PC-14B include event counting, one-shot pulse generation, rate generation, event timing, strobe, frequency generation and external synchronization.

Features

- 48 TTL compatible Digital I/O Lines
- Three independent Counter Timers
- Timer Clock input programmable from four different sources
- On board 2MHz reference oscillator
- Programmable interrupt requests
- Dedicated external interrupt line
- Onboard wait-state generation

Specifications

Digital I/O	
Number of lines:	48, in 6x 8bits
Compatibility:	TTL
Input Logic Low:	0.5V to 0.8V
Input Logic High:	2V to 5V
Input Load Current:	±10 A max.
Current Sink:	I_{sink} = 1.7mA @ 0.45V
Current Source:	I_{source} = 200 A @ 2.4V
Output Drive Current:	4mA max @ V_{out} = 1.5V
Counter/Timers	
Number:	3 Independent
Compatibility:	TTL
Resolution:	16bit
Input Frequency:	8MHz max
On-board Clock:	2MHz
External Interrupt	
Compatibility:	TTL
Sensitivity:	Rising Edge
Pulse Width:	20 nS minimum
PC Interface	
Wait States:	0, 1, 2, 4 or 8
Operating temp:	0°C to 55°C

Ordering Information

Supplied with EDR Software and Internal Cable for Counter/Timers (IDC26 to DB25)
PC-14B 48 Channel Digital I/O Card with 3 Counter Timers
View connector pinouts in Reference Section

Optional Accessories

Refer to System Diagram in Reference Section
1. ADPT-5050 via **IDC50 (Digital I/O)**
2. ADPT-2526 via **IDC26 or DB25MF (Digital Counter Timers)**