## Multifunction data acquisition board, isolated, 16-bit











LabWindows/CVI™

DIA*dem*\*

## **Features**

## Analog inputs

- 16 single-ended/8 differential or 8 single-ended/4 differential inputs
- Resolution: 16-bit
- Conversion time: 10 µs
- Overvoltage protection ± 20 V
- Input range: 0-10 V, ± 10 V software programmable, 0-20 mA optional
- Low-pass filter and current inputs as option
  Gain: 1, 2, 5, 10, freely programmable through
- software for each channel
- 16-bit DMA access for analog data acquisition
- 3 timers: timer 0 and timer 1 only for the analog acquisition, timer 2 for the analog acquisition or as watchdog

### Analog acquisition

- Acquisition of one single channel, several channels
   or several channels through scan list
- Automatic analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, externals event
- Trigger functions:
  - software trigger or
  - external trigger: the analog acquisition (single or scan) is started through a signal switching from 0 to 24 V on digital input 0.
- Interrupt: end of single channel, end of multichannel, end of scan list

## Analog outputs

- 4 or 8 analog outputs
- Resolution: 12-bit
- Reset to definite level
- Watchdog (Timer 2): the analog outputs are reset to "0" after watchdog overflow
- Setting time: typ. 6 µs (0-10 V)
- Simultaneous actualisation of the outputs
- + Output voltage range: 0-10 V,  $\pm 10$  V
- Output current typ. ±5 mA
- Driver for high capacitive loads (500 pF)
- Each output has its own ground line

### Safety features

- Optical isolation 500 V
- Noise neutralization of the PC voltage supply

## PA 3110

16 single-ended/8 diff. inputs, 16-bit

4/8 analog outputs, 12-bit

**Optical isolation 500 V** 

Automatic analog acquisition

Simultaneous actualisation of the outputs

Reset to definite output level

## 100 kHz data transfer rate

**Onboard FIFO, DMA access** 

Creeping distance IEC 61010-1 (VDE411-1)

## EMC tested acc. to 89/336/EEC

• IEC 61326: electrical equipment for measurement, control and laboratory use

## Applications

- Industrial process control
- Industrial measurement
- Automatic test equipment
- Temperature monitoring and control
- Control of chemical processes
- Factory automation
- Acquisition of sensor data
- Laboratory equipment
- Current measurement

## Software drivers

A CD-ROM with the following software and programming examples is supplied with the board.

### Standard drivers for:

Windows 2000/NT/98/95, Windows 3.11, MS-DOS Real-time drivers for 2000/NT/98/95

### **Drivers for the following application software:** LabVIEW 5.01

### Samples for the following compilers:

Microsoft VC++ 5.0 Microsoft C 6.0 Borland C++ 5.01 Borland C 3.1 Visual Basic 5.0 Visual Basic 1.0 Delphi 4 Turbo Pascal 7.0

## On request:

DiaDem 6/7 LabWindows/CVI 5.01

Current driver list on the web: www.addi-data.com

# Multifunction data acquisition board, isolated, 16-bit



## PA 3110

#### **Specifications**

16 single-ended/8 differential or

0-10 V, ±10 V adjustable for each channel

 $10^{12} \Omega //10$  nF single-ended,  $10^{12} \Omega //20$  nF

through software or programmable timer

IRQ 3, 5, 9 for XT, IRQ 10, 11, 12, 14, 15 for AT

0.7  $\mu s$ ; smallest programmable value: 10  $\mu s$ 

70  $\mu s$ ; smallest programmable value: 140  $\mu s$ 

70 µs; smallest programmable value: 140 µs

from 6 to 10 µs depending on voltage spikes

 $\pm \frac{1}{2}$  LSB max. at 25 °C,  $\pm 1$  LSB through

±1/2 LSB max. at 25 °C, ±1 LSB through

through software; 0-20 mA optional

Programmable gain (x1, x2, x5, x10)

DC at 60 Hz, 90 dB minimum

differential against GND

4 or 8 analog outputs

and temperature range

 $\pm 5$  mA/500 pF, 2 k $\Omega$ 

temperature range

temperature range

8 single-ended/4 differential

500 V

16-bit

10 µs

±4 LSB

± 15-bit

5, 6, 7

12-bit

±12 V

±25 mA

0-10 V, ±10 V

± 12 V at power-on

### Analog inputs Number of inputs:

Optical isolation: Resolution: Input range:

Conversion time: Gain: Overvoltage protection: Common mode rejection: Relative precision (INL): Diff. Non-linearity (DNL): Input impedance (PGA):

Conversion start: Interrupts: DMA: Timer configuration Timer 0: Timer 1: Timer 2:

#### Analog outputs

Number of outputs: Resolution: Output range: Setting time at 2 kΩ, 1000 pF:

Overvoltage protection: Max. output current/load: Short-circuit current: Integral non-linearity (INL):

Diff. non-linearity (DNL):

#### Noise immunity

Test level:

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- ESD: 4 kV - Fields: 10 V/m - Burst: 2 kV/4 kV Netz - Conducted radio interferences: 10 V

## Physical and environmental conditions

Dimensions:	230 x 114 mm
System bus:	ISA
Place required:	1 AT slot
Operating voltage:	+5 V, ±5 %
Current consumption:	PA 3110-16-8: 1180 mA typ. ± 10 % typ.
Front connector:	37-pin SUB-D male connector
emperature range:	0 to 60 °C (with forced cooling)

Simplified block diagram



## Pin assignment – 37-pin SUB-D male connector

DIFF SE		$\sim$		SE	DIFF
An. output 7           An. output 6           An. output 6           An. output 5           An. output 4           An. output 1           Anolog input GND           Annipp. 4           (An. inp. 5           (+) An. inp. 1           (+) An. inp. 7           (+) An. inp. 6           (+) An. inp. 6	19 18 17 16 15 14 13 12 11 10 9 9 8 3 7 4 6 5 5 5 5 4 6 5 5 5 1 4 4 6 5 5 5 1 4 1 6 5 5 1 2 1 1 7 1 1 7 1 6 1 5 1 2 1 1 7 1 7 1 7 1 6 1 5 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7 1 7		37 36 35 34 33 32 31 30 29 28 27 26 25 24 25 24 22 21 20	An. outpu           An. nup. 5           (+) An. inp. 5           (+) An. inp. 3           (+) An. inp. 3           (+) An. inp. 1           (+) An. inp. 1           (+) An. inp. 1	t 7 GND t 6 GND t 6 GND t 4 GND t 3 GND t 2 GND t 2 GND t 2 GND t 3 GND t 1 GND t 1 GND t 1 GND t 0 GND t 1 GND () An inp. 0 () An inp. 3 () An inp. 3 () An inp. 3 () An inp. 0

1: The analog inputs have a common ground line 2: The analog outputs have separate ground lines

## **ADDI-DATA** connection



Terminal board PX 901-AG with cable ST010

## **ORDERING INFORMATION**

## ADDIALOG PA 3110

Isolated multifunction data acquisition board. Incl. technical description and software drivers.

#### Versions

PA 3110-16-4	16 SE/8 diff. inputs, 4 analog outputs
PA 3110-16-8	16 SE/8 diff. inputs, 8 analog outputs
PA 3110-8-4	8 SE/4 diff. inputs, 4 analog outputs
PA 3110-8-8	8 SE/4 diff. inputs, 8 analog outputs

## Options

Please specify the number of channels to be supplied with the required option

Option SF:	Filter for 1 single-ended input
Option DF:	Precision filter for 1 differential input
Option PC:	Current input 0(4)-20 mA and precision
	250 $\Omega$ ; Tol. 0.01 %; TK 5; ½ $\Omega$ ;

**Option PC-SE:** for 1 single-ended input **Option PC-Diff:** for 1 differential input

#### Connection

PX 901-A:	Screw terminal board with transorb diodes,
	for connecting the analog inputs
PX 901-AG:	Screw terminal board for DIN rail
ST010:	Standard round cable, shielded, twisted pairs, 2 m
ST011:	Standard round cable, shielded, twisted pairs, 5 m

www.addi-data.com Sales: +49(0)7223/9493-120 Fax: +49(0)7223/9493-92