

Each module requires 64 bytes. 32-bit commands can be read or written on these modules.

Table 1: I/O mapping of the function modules

	IORD / IOWR			
	D31...D24	D23...D16	D15.....D8	D7.....D0
BYTES	HIGHBYTE	MIDHIGHBYTE	MIDLOWBYTE	LOWBYTE
BASE+0	FUNCTION MODULE 1			
BASE+63	FUNCTION MODULE 1			
BASE+64	FUNCTION MODULE 2			
BASE+127	FUNCTION MODULE 2			
BASE+128	FUNCTION MODULE 3			
BASE+191	FUNCTION MODULE 3			
BASE+192	FUNCTION MODULE 4			
BASE+256	FUNCTION MODULE 4			

Function module 1 can occupy the following addresses: Base +0 to Base + 63.

Function module 2 can occupy the following addresses: Base +64 to Base + 127.

Function module 3 can occupy the following addresses: Base +128 to Base + 191.

Function module 4 can occupy the following addresses: Base +192 to Base + 255.

Table 2: I/O mapping of the TTL I/O

Read					Write			
Counter-Number Module number					Function module 0)			
D31...D24		D23...D16	D15.....D8	D7.....D0	D31...D24	D23...D16	D15.....D8	D7.....D0
BYTES	HIGHBYTE	MIDHIGHB	MIDLOWB	LOWBYTE	HIGHBYTE	MIDHIGHB	MIDLOWB	LOWBYTE
BASEx + 0	Port D J,I	Port C as input	Port B as input	Port A as input	-	-	-	output I D0
BASEx + 4	-	-	-	-	-	-	-	output J D0
BASEx + 8	-	-	-	-	-	-	-	Port A as output
BASEx + 12	-	-	-	-	-	-	-	Port B as output
BASEx + 16	-	-	-	-	-	-	-	Port C as output
BASEx + 20	-	-	-	Control Word [D3..0]	-	-	-	Control Word D[3..0]
BASEx + 60	Module indentification „TL20“				-	-	-	-

-: no function; **BASEx**: Base address of the function module number x.

The channels of ports A, B, C are set as inputs after reset, the channels of port D (I, J) are set as outputs after reset.